



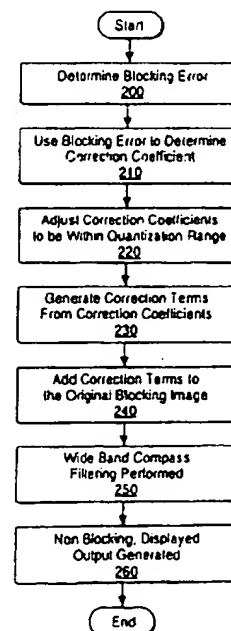
INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(51) International Patent Classification ⁶: H04N 7/30	A1	(11) International Publication Number: WO 97/40627 (43) International Publication Date: 30 October 1997 (30.10.97)
(21) International Application Number: PCT/US97/03292 (22) International Filing Date: 3 March 1997 (03.03.97) (30) Priority Data: 08/637,206 24 April 1996 (24.04.96) US (71) Applicants (for all designated States except US): SONY CORPORATION [JP/JP]; 7-35, Kitashinagawa 6-chome, Shinagawa-ku, Tokyo 141 (JP). SONY ELECTRONICS, INC. [US/US]; 1 Sony Drive, Park Ridge, NJ 07656 (US). (72) Inventors; and (75) Inventors/Applicants (for US only): CHANG, Ching-Fang [-/US]; 6560 Capri Way, San Jose, CA 95129 (US). LEE, Chuen-Chien [-/US]; 40200 San Sebastian Place, Fremont, CA 94539 (US). (74) Agents: SOBRINO, Maria, E. et al.; Blakely, Sokoloff, Taylor & Zafman, 7th floor, 12400 Wilshire Boulevard, Los Angeles, CA 90025-1026 (US).		(81) Designated States: AL, AM, AT, AT (Utility model), AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CU, CZ, CZ (Utility model), DE, DE (Utility model), DK, DK (Utility model), EE, EE (Utility model), ES, FI, FI (Utility model), GB, GE, GH, HU, IL, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SK (Utility model), TJ, TM, TR, TT, UA, UG, US, UZ, VN, YU, ARIPO patent (GH, KE, LS, MW, SD, SZ, UG), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, ML, MR, NE, SN, TD, TG). Published <i>With international search report.</i>

(54) Title: METHOD AND APPARATUS FOR BLOCKING EFFECT REDUCTION IN IMAGES BY POST-PROCESSING IN THE SPATIAL DOMAIN

(57) Abstract

An apparatus and method for post-processing image data which previously was encoded using a discrete cosine transform in order to remove resulting blocking effects. Correction coefficients are generated by determining the least mean square error of the blocking error. The correction coefficients are adjusted to be within the quantization range of the quantization step size used during the coding process. The adjusted correction coefficients are then used to generate the correction terms which are subsequently combined with the original image data to generate an image exhibiting reduced blocking effects when the image is displayed.



FOR THE PURPOSES OF INFORMATION ONLY

Codes used to identify States party to the PCT on the front pages of pamphlets publishing international applications under the PCT.

AL	Albania	ES	Spain	LS	Lesotho	SI	Slovenia
AM	Armenia	FI	Finland	LT	Lithuania	SK	Slovakia
AT	Austria	FR	France	LU	Luxembourg	SN	Senegal
AU	Australia	GA	Gabon	LV	Latvia	SZ	Swaziland
AZ	Azerbaijan	GB	United Kingdom	MC	Monaco	TD	Chad
BA	Bosnia and Herzegovina	GE	Georgia	MD	Republic of Moldova	TG	Togo
BB	Barbados	GH	Ghana	MG	Madagascar	TJ	Tajikistan
BE	Belgium	GN	Guinea	MK	The former Yugoslav Republic of Macedonia	TM	Turkmenistan
BF	Burkina Faso	GR	Greece	ML	Mali	TR	Turkey
BG	Bulgaria	HU	Hungary	MN	Mongolia	TT	Trinidad and Tobago
BJ	Benin	IE	Ireland	MR	Mauritania	UA	Ukraine
BR	Brazil	IL	Israel	MW	Malawi	UG	Uganda
BY	Belarus	IS	Iceland	MX	Mexico	US	United States of America
CA	Canada	IT	Italy	NE	Niger	UZ	Uzbekistan
CF	Central African Republic	JP	Japan	NL	Netherlands	VN	Viet Nam
CG	Congo	KE	Kenya	NO	Norway	YU	Yugoslavia
CH	Switzerland	KG	Kyrgyzstan	NZ	New Zealand	ZW	Zimbabwe
CI	Côte d'Ivoire	KP	Democratic People's Republic of Korea	PL	Poland		
CM	Cameroon	KR	Republic of Korea	PT	Portugal		
CN	China	KZ	Kazakhstan	RO	Romania		
CU	Cuba	LC	Saint Lucia	RU	Russian Federation		
CZ	Czech Republic	LI	Liechtenstein	SD	Sudan		
DE	Germany	LK	Sri Lanka	SE	Sweden		
DK	Denmark	LR	Liberia	SG	Singapore		
EE	Estonia						

METHOD AND APPARATUS FOR BLOCKING EFFECT REDUCTION IN IMAGES
BY POST-PROCESSING IN THE SPATIAL DOMAIN

BACKGROUND OF THE INVENTION

1. Field of the Invention

The present invention relates to the removal of blocking effects in images previously compressed and decompressed. More particularly, the present invention relates to the removal of blocking effects in images compressed in accordance with MPEG, JPEG and other DCT based image coding formats.

2. Art Background

The need to compress digital image data, whether static or dynamic (i.e., video images) images has dramatically increased. One reason is the dramatic popularity of multimedia computer systems and software. Another reason is switch to digital broadcasts of station programming. One example of the latter is direct satellite broadcasts, such as DSSTTM. In order to minimize the bandwidth required to transmit broadcasts of programs or to minimize the amount of storage space required to store certain images, compression techniques are utilized. Thus, the image data is transmitted or stored in a

compressed format and prior to the display of the image, the image data is decompressed. Examples of widely utilized compression algorithms are those that comply with the MPEG (Moving Pictures Experts Group) and JPEG (Joint Picture Experts Group) standards.

Many compression processes, including those that comply with MPEG or JPEG standards, utilize transform coding. In a transform coding process, an image is divided into small blocks. The transform of each block is taken, the coefficients of which are quantized in accordance with a determined quantization factor q . The most popular transform is the discrete cosine transform (DCT).

However, one negative effect of the process is referred to as the "blocking effect". By dividing the image into blocks prior to encoding, discontinuities (referred to as blocking effects) between adjacent blocks occurs through the encoding and decoding processes. This is represented in a displayed decompressed image by clear jumps between colors or greyscales as opposed to a smooth change.

Pre-processing and post-processing techniques are utilized to minimize blocking effects. Pre-processing techniques dictate that the originator of the image data must perform certain steps to minimize blocking effects. Post-processing techniques, although logistically better as the correction is performed after decompression, has its problems. For example, one of the simplest techniques is to process the decompressed image data through a low pass filter. Although the blocking effects are decreased, the sharpness of the displayed image is negatively affected.

SUMMARY OF THE INVENTION

In the method and circuit of the present invention post processing is performed on decompressed image data to minimize blocking effects without affecting the sharpness of the image.

To minimize the blocking effect, correction terms are added to the image. The correction terms are added to the blocking image such that the mean square of the blocking error is reduced. Therefore, in one embodiment the blocking error is first determined. Using the blocking error, correction coefficients are generated. These correction coefficients are preferably constrained to be within ranges dictated by the quantization values used in the original compression process. The adjusted correction coefficients are then used to generate the correction terms which are subsequently combined with the original image data to generate an image exhibiting reduced blocking effects when the image is displayed.

BRIEF DESCRIPTION OF THE DRAWINGS

The objects, features and advantages of the present invention will be apparent to one skilled in the art in light of the following detailed description in which:

Figure 1 is a simplified block diagram of a system that operates in accordance with the teachings of the present invention.

Figure 2 is a flow chart depicting the process for minimizing blocking effects.

Figure 3 is a diagram illustrating the process to be performed on a block by block basis.

Figure 4 is a simplified block diagram of a circuit that performs a process for minimizing blocking effects.

DETAILED DESCRIPTION

The present invention provides a simple but effective apparatus and method for minimizing blocking effects that occur in discrete cosine transformed images. In the following description, for purposes of explanation, numerous details are set forth, in order to provide a thorough understanding of the present invention. However, it will be apparent to one skilled in the art that these specific details are not required in order to practice the present invention. In other instances, well known electrical structures and circuits are shown in block diagram form in order not to obscure the present invention unnecessarily.

A simplified block diagram of an exemplary system that operates in accordance with the teachings of the present invention is illustrated in **Figure 1**. To minimize on transmission bandwidth and/or to save on the amount of space required to store an image, the image is frequently formatted in a compressed form which utilizes discrete transforms. Popular examples of formats include those that comply with the JPEG (Joint Picture Experts Group) and MPEG (Motion Picture Experts Group) standards. The system illustrated includes the receiver/display system 10 and display 40. One example of a receiver/display system 10 is a Video CD player. Another example is a direct satellite receiver such as one manufactured by Sony Corporation. Other types of receivers/players/storage/display systems are also contemplated.

The receiver 10 receives the compressed image. The receiver may be one of many types of receiving devices configured to receive image data. Alternately, the receiver may be one to receive television broadcast signals or a device coupled to directly to a storage unit (e.g., memory, VCR, CD ROM or the like) to receive images retrieved from storage. For purposes of explanation, the image data is received in a format compatible with the MPEG specification; however, the other types of compressed formats that use discrete transforms to compress the data can also be used.

Once the image data is received, the data is decoded (i.e., decompressed) by decoder 20 and processed by post-processor 30 in order to remove blocking effects created by the encoding and decoding

processes. The modified image data subsequently displayed by display subsystem 40 shows an image in which the blocking effects are minimized.

In the present invention the blocking error is used to find correction coefficients which are adjusted to be within the quantization range utilized during the encoding process. Correction terms are generated from the adjusted correction coefficients. The correction terms are added to the image containing the blocking effects to minimize the blocking effects. Preferably, this process is performed on a block by block basis. The block size is determined by the block size utilized to perform the DCT process during encoding. However, it is readily apparent that the process can be performed on differing unit sizes.

Referring to the flow chart of Figure 2, at step 200, the image data received is examined to determine the blocking error. The blocking error is the estimated error between an image with (the blocking image) and an image without blocking effects. The blocking error can be calculated a number of ways. In one embodiment, the blocking error is obtained by determining the difference between luminance values of the blocking image and the blocking image processed through a low pass filter. An example of a low pass filter is a 5x5 median filter (For further information on median filters, see Anil K. Jain Fundamentals of Digital Image Processing, (1989 Prentice-Hall) pp. 244-249). The low pass filter functions to remove some blocking effects. In another embodiment, the blocking error is defined

as the step or difference between pixel values (e.g., luminance values) across block boundaries.

In the present example, the blocking error is determined for each pixel within a block. A sample block is illustrated in **Figure 3**. Referring to **Figure 3**, the blocking image is formed of a plurality of blocks 305 dictated in size by the block size utilized to perform the DCT during encoding. In the present example the block size is defined to be $N \times M$, where M is equal to 4 and N is equal to 8. A particular block is identified by the coordinate of a predetermined point within the block. In the present illustration, the coordinate (x_b, y_b) defines the lower right corner of the block 310 having the dimensions $N \times M$. Within the block there are $N \times M$ pixels. For each pixel $p[i][j]$ of the blocking image and each pixel of the low pass filtered image $LPF_p[i][j]$, the blocking error $E[i][j]$ within a block is determined by:

$$E[i][j] = p[x_b \cdot N + i][y_b \cdot M + j] - LPF_p[x_b \cdot N + i][y_b \cdot M + j]$$

where $0 \leq i < N$, $0 \leq j < M$

It follows that after the correction terms have been added, the blocking error would be:

$$\text{Blocking Error} = E[i][j] + A[i][j]$$

where $0 \leq i < N$, $0 \leq j < M$, and $A[i][j]$ is the correction term.

The correction term consist of a linear combination of several basis functions:

$$A[i][j] = \sum_{k=0}^{k_x-1} \sum_{l=0}^{l_x-1} a_{kl} \phi_{kl}[i][j],$$

where $\phi_{kl}[\][\]$ are the basis functions, a_{kl} are the correction coefficients, $0 \leq k < k_x, 0 \leq l < l_x, k_x * l_x$ is equal to the number of correction coefficients. Preferably, the values of k_x and l_x are chosen to minimize the number of correction coefficients. For further information on basis functions, see e.g., Anil K. Jain, Fundamentals of Digital Image Processing, Chapter 5, pp. 132 - 180 (Prentice-Hall 1989).

The correction coefficients are determined by applying the least mean square error (LMSE) criterion on the blocking error. Therefore, in order to generate correction coefficients a_{kl} , the LMSE criterion is applied to the blocking terms after correction:

$$\frac{\partial MSE}{\partial a_{kl}} = 0$$

where $0 \leq k < k_x, 0 \leq l < l_x$, and MSE is the mean square error of the block.

The blocking errors generated are then utilized to generate correction coefficients. Furthermore, although any basis function can be used, it is preferred that the basis function is chosen to minimize the number of correction coefficients. Therefore, since the transform used in the present example is DCT-based, the basis function used is a cosine based function. Thus, continuing with the present example, the LMSE criterion is applied to the blocking errors of all the pixels in the block being processed where the MSE is defined as:

$$\text{MSE} = \frac{1}{MN} \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} (E[i][j] + A[i][j])^2$$

where the basis function $\phi_{kl} [i][j]$ is chosen as :

$$\phi_{kl} [i][j] = \cos \frac{(2i+1)k\pi}{2N} \cdot \cos \frac{(2j+1)l\pi}{2M},$$

where $0 \leq k < N, 0 \leq l < M$

Thus, the MSE can be expressed as:

$$\text{MSE} = \frac{1}{MN} \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} \left(E[i][j] + \sum_{k=0}^{N-1} \sum_{l=0}^{M-1} a_{kl} \cdot \cos \left[\frac{(2i+1)k\pi}{2N} \right] \cdot \cos \left[\frac{(2j+1)l\pi}{2M} \right] \right)^2$$

By setting the first partial derivative with respect to the correction

coefficient equal to zero (i.e., $\frac{\partial \text{MSE}}{\partial a_{kl}} = 0$), the correction coefficient can

be determined:

$$a_{kl} =$$

$$-\frac{1}{MN} \left\{ \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} E[i][j] \cdot \cos \left[\frac{(2i+1)k\pi}{2N} \right] \cdot \cos \left[\frac{(2j+1)l\pi}{2M} \right] \right\}, \text{ if } k=0, l=0$$

$$-\frac{2}{MN} \left\{ \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} E[i][j] \cdot \cos \left[\frac{(2i+1)k\pi}{2N} \right] \cdot \cos \left[\frac{(2j+1)l\pi}{2M} \right] \right\}, \text{ if } k=0, l \neq 0 \text{ or}$$

$$k \neq 0, l=0$$

$$-\frac{4}{MN} \left\{ \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} E[i][j] \cdot \cos \left[\frac{(2i+1)k\pi}{2N} \right] \cdot \cos \left[\frac{(2j+1)l\pi}{2M} \right] \right\}, \text{ if } k \neq 0, l \neq 0$$

Alternately, the LMSE criterion is applied to the blocking errors of only the pixels located along the block boundaries in the block.

Thus the MSE is defined as:

$$MSE = \frac{1}{2(M+N)} \left\{ \sum_{i=0}^{N-1} (E[i][0] + A[i][0])^2 + \sum_{i=0}^{N-1} (E[i][M-1] + A[i][M-1])^2 + \sum_{j=0}^{M-1} (E[0][j] + A[0][j])^2 + \sum_{j=0}^{M-1} (E[N-1][j] + A[N-1][j])^2 \right\}$$

Expanding the equation results in:

$$MSE = \frac{1}{2(M+N)} \left\{ \sum_{i=0}^{N-1} \left(E[i][0] + \sum_{k=0}^{kx-1} \sum_{l=0}^{lx-1} a_{kl} \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{l\pi}{2M}\right] \right)^2 + \sum_{i=0}^{N-1} \left(E[i][M-1] + \sum_{k=0}^{kx-1} \sum_{l=0}^{lx-1} a_{kl} \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2M-1)l\pi}{2M}\right] \right)^2 + \sum_{j=0}^{M-1} \left(E[0][j] + \sum_{k=0}^{kx-1} \sum_{l=0}^{lx-1} a_{kl} \cdot \cos\left[\frac{k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right] \right)^2 + \sum_{j=0}^{M-1} \left(E[N-1][j] + \sum_{k=0}^{kx-1} \sum_{l=0}^{lx-1} a_{kl} \cdot \cos\left[\frac{(2N-1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right] \right)^2 \right\}$$

For example, if $kx = 2$ and $lx = 2$, four correction coefficients, a_{00} , a_{01} , a_{10} , a_{11} , are used to generate a correction term. Therefore, the first partial derivative of the MSE with respect to the correction coefficient a_{kl} is set to equal zero ($\frac{\partial MSE}{\partial a_{kl}} = 0$) in order to determine the correction

coefficients a_{kl}

($0 \leq k < 2; 0 \leq l < 2$):

$$a_{00} = \frac{-1}{2(M+N)} \left\{ \sum_{i=0}^{N-1} (E[i][0] + E[i][M-1]) + \sum_{j=0}^{M-1} (E[0][j] - E[N-1][j]) \right\}$$

$$a_{10} = \frac{-1}{N + 2M \cos^2 \frac{\pi}{2N}} \left\{ \sum_{i=0}^{N-1} \cos \left[\frac{(2i+1)\pi}{2N} \right] (E[i][0] + E[i][M-1]) + \cos \frac{\pi}{2N} \sum_{j=0}^{M-1} (E[0][j] - E[N-1][j]) \right\}$$

$$a_{01} = \frac{-1}{M + 2N \cos^2 \frac{\pi}{2M}} \left\{ \sum_{j=0}^{M-1} \cos \left[\frac{(2j+1)\pi}{2M} \right] (E[0][j] + E[N-1][j]) + \cos \frac{\pi}{2M} \sum_{i=0}^{N-1} (E[i][0] - E[i][M-1]) \right\}$$

$$a_{11} = \frac{-1}{N \cos^2 \frac{\pi}{2M} + M \cos^2 \frac{\pi}{2N}} \left\{ \cos \frac{\pi}{2M} \sum_{i=0}^{N-1} \cos \left[\frac{(2i+1)\pi}{2N} \right] (E[i][0] - E[i][M-1]) + \cos \frac{\pi}{2N} \sum_{j=0}^{M-1} \cos \left[\frac{(2j+1)\pi}{2M} \right] (E[0][j] - E[N-1][j]) \right\}$$

At step 220, in the frequency domain, the correction coefficients are adjusted to be within the original quantization range defined by quantization steps used in the encoding process. This range is preferably defined to be plus and minus one-half of the quantization step. Thus, if the correction coefficient is within the quantization range specified by plus or minus one half of the quantization step, the adjusted value of the correction coefficient equals the original value of the correction coefficient. If the correction coefficient is outside the quantization range specified by plus or minus one half of the quantization step, the correction coefficient is updated to be the

nearest boundary of the range (i.e., $\pm q/2$, where q is the quantization step performed with respect to the correction coefficients).

Once the correction coefficients have been adjusted, the correction terms $A[i][j]$ are generated, step 230. The correction terms are preferably generated according to the following equation:

$$A[i][j] = \sum_{k=0}^{k_x-1} \sum_{l=0}^{l_x-1} a_{kl} \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right]$$

where $0 \leq i \leq N$, $0 \leq j \leq M$, $0 \leq k_x \leq N$, $0 \leq l_x \leq M$, and $k_x \cdot l_x$ is the number of correction coefficients used to generate the correction term.

At step 240, the correction terms are added to the original blocking image to produce an image containing a decrease in blocking effects. For example, the adjusted image $p'[i][j]$ is determined:

$$p'[i][j] = p[i][j] + A[i][j],$$

where $p[i][j]$ is pixel data of the original blocking image, $p'[i][j]$ is pixel data of the produced image exhibiting reduced blocking effects, and $0 \leq i \leq N$, $0 \leq j \leq M$.

Preferably, at step 250, wide band low pass filtering is applied to remove some of the artifacts introduced by adding the correction terms to the blocking image. For example a low pass filter is used to remove only very high frequency components while preserving the sharpness of the image as much as possible. In the current embodiment, a 3x3 median filter is used. For further information on median filters, see for example, Anil K. Jain, Fundamentals of Digital Image Processing (1989 Prentice-Hall Inc.) pp 244-249. Once filtered,

the image can be processed for display, step 260. The resulting image displayed will reveal minimal blocking effect while maintaining a sharp image.

A simplified block diagram of an exemplary post processing circuit is illustrated in Figure 4. It is apparent that the circuit can be implemented a variety of ways to achieve the postprocessing function. For example, the process can be implemented in a general purpose or dedicated computer system executing software to perform the steps described.

Referring now to Figure 4, the post processing circuit includes a first low pass filter (LPF) 405, first delay 410, subtraction circuit 415, correction coefficient circuit 420, Q-level adjustment subcircuit 425, correction term circuit 430, second delay 435, addition circuit 440 and second LPF 445.

The first LPF 405 functions to filter out the high frequency components from the image data. For example, a 5x5 median filter can be used. As the filtered image data and original image data are subsequently input to the subtraction circuit 415, a delay 410 is preferably placed in the circuit to keep the original image data in synchronization with the filtered image data. Thus, the length of the delay 410 is approximately equal to the amount of time it takes to process the image data through the LPF 405. Both the original image data output by delay 410 and the filtered image data output by LPF 405 are input to a subtraction circuit 415 which generates the blocking error by subtracting the luminance values of the pixels of the blocking

image from the luminance values of the corresponding pixels of the filtered image.

The blocking error determined by subtraction circuit 415 is input to correction coefficient 420 which generates the correction coefficients as discussed above. The correction coefficients generated are input to Q-level adjustment circuit 425 which adjusts the correction coefficients, as necessary, to be within the quantization range specified by the quantization step size utilized during the encoding process. The adjusted correction coefficients are used by correction term circuit 430 to generate the correction terms.

The correction terms are added to the original blocking image by addition circuit 440. To provide the timing of inputs to the circuit 440, a delay circuit 435 is used to delay the blocking image data from reaching the addition circuit 440 until the correction terms are generated. The second LPF is a wider band LPF that is used to remove artifacts generated during the addition process. The second LPF is chosen so that only very high frequency components are removed, while sharpness is preserved as much as possible. For example, a 3x3 median filter can be used.

The invention has been described in conjunction with the preferred embodiment. It is evident that numerous alternatives, modifications, variations and uses will be apparent to those skilled in the art in light of the foregoing description.

CLAIMS

What is claimed is:

1. A method for minimizing blocking effects in a blocking image caused by prior performance of encoding/decoding processes including a transform and quantization process, said blocking image described by blocking image data, said method comprising the steps of:
 - determining blocking errors in the blocking image data;
 - generating correction coefficients in accordance with the least mean square error of the blocking errors;
 - calculating correction terms using the correction coefficients;
 - combining the blocking image data and the correction terms to generate an adjusted image data.
2. The method as set forth in claim 1, further comprising the step of adjusting the correction coefficients to be within quantization range defined by the quantization step size utilized in the coding process;
 - wherein the step of calculating correction terms is performed using adjusted correction coefficients.

3. The method as set forth in claim 1, wherein the step of determining blocking errors comprises the steps of:

filtering the blocking image data to remove high frequency components above a first identified threshold to generate a filtered image data; and

subtracting the filtered image data from the blocking image data to generate the blocking errors.

4. The method as set forth in claim 3, wherein the first identified threshold is chosen such that most of the blocking effects are removed, at a cost of sharpness of the image.

5. The method as set forth in claim 1, further comprising the step of wide band filtering above a second identified threshold the combined image data to remove artifacts caused by the step of combining the blocking image data and the correction terms to generate an adjusted image data.

6. The method as set forth in claim 5, wherein the second identified threshold is chosen such that only very high frequency components are removed, while sharpness of the image is preserved as much as possible.

7. The method as set forth in claim 1, wherein the step of generating correction coefficients comprises the step of applying a Least Mean Square Error (LMSE) criterium to the Mean Square Error (MSE) of the blocking errors by solving the following to determine the correction coefficients a_{kl} : $\frac{\partial MSE}{\partial a_{kl}} = 0$ where $0 \leq k < K, 0 \leq l < L, K \times L$ is equal to the number of correction coefficients used in the correction terms, and a_{kl} are the correction coefficients.

8. The method as set forth in claim 7, wherein the MSE is chosen to be the blocking errors of all pixels in the block as defined by the following equation:

$$\begin{aligned} \text{MSE} &= \frac{1}{MN} \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} (E[i][j] + A[i][j])^2 \\ &= \frac{1}{MN} \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} \left(E[i][j] + \sum_{k=0}^{K-1} \sum_{l=0}^{L-1} a_{kl} \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right] \right)^2 \end{aligned}$$

and the resulting solutions of the correction coefficients a_{kl} are determined according to the following equations:

$$\begin{aligned} a_{kl} &= \frac{-1}{MN} \left\{ \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} E[i][j] \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right] \right\}, \text{ if } k=0, l=0 \\ &= \frac{-2}{MN} \left\{ \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} E[i][j] \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right] \right\}, \text{ if } k=0, l \neq 0 \text{ or } \\ &\quad k \neq 0, l=0 \\ &= \frac{-4}{MN} \left\{ \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} E[i][j] \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right] \right\}, \text{ if } k \neq 0, l \neq 0 \end{aligned}$$

where $E[i][j]$ is the blocking error, $A[i][j]$ is the correction term, and M, N represent the dimensions of the block size.

9. The method as set forth in claim 7, wherein the MSE can be chosen to be the blocking errors of the pixels along the block boundaries as defined by the following equation:

MSE=

$$\frac{1}{2(M+N)} \left\{ \begin{aligned} & \sum_{i=0}^{N-1} (E[i][0] + A[i][0])^2 \\ & + \sum_{i=0}^{N-1} (E[i][M-1] + A[i][M-1])^2 \\ & + \sum_{j=0}^{M-1} (E[0][j] + A[0][j])^2 \\ & + \sum_{j=0}^{M-1} (E[N-1][j] + A[N-1][j])^2 \end{aligned} \right\}$$

where $E[][]$ represents the blocking error, $A[][]$ represents the correction turn and M, N are the dimensions of the block size.

10. The method as set forth in claim 7 wherein if $k_x=2$ and $l_x=2$, the resulting solutions of the four correction coefficients, a_{00} and a_{01} , a_{10} , a_{11} , are determined according to the following equation:

$$\begin{aligned}
 a_{00} &= \frac{-1}{2(M+N)} \left\{ \sum_{i=0}^{N-1} (E[i][0] + E[i][M-1]) + \sum_{j=0}^{M-1} (E[0][j] - E[N-1][j]) \right\} \\
 a_{10} &= \frac{-1}{N + 2M \cos^2 \frac{\pi}{2N}} \left\{ \sum_{i=0}^{N-1} \cos \left[\frac{(2j+1)\pi}{2N} \right] (E[i][0] + E[i][M-1]) + \right. \\
 &\quad \left. \cos \frac{\pi}{2N} \sum_{j=0}^{M-1} (E[0][j] - E[N-1][j]) \right\} \\
 a_{01} &= \frac{-1}{M = 2N \cos^2 \frac{\pi}{2M}} \left\{ \sum_{j=0}^{M-1} \cos \left[\frac{(2j+1)\pi}{2M} \right] (E[0][j] + E[N-1][j]) + \right. \\
 &\quad \left. \cos \frac{\pi}{2M} \sum_{i=0}^{N-1} (E[i][0] - E[i][M-1]) \right\} \\
 a_{11} &= \frac{-1}{N \cos^2 \frac{\pi}{2M} + M \cos^2 \frac{\pi}{2N}} \left\{ \cos \frac{\pi}{2M} \sum_{j=0}^{N-1} \cos \left[\frac{(2i+1)\pi}{2N} \right] (E[i][0] - E[i][M-1]) + \right. \\
 &\quad \left. \cos \frac{\pi}{2N} \sum_{j=0}^{M-1} \cos \left[\frac{(2j+1)\pi}{2M} \right] (E[0][j] - E[N-1][j]) \right\}
 \end{aligned}$$

where $E[][]$ represents the blocking error and M, N are the dimensions of the block size.

11. The method as set forth in claim 2, wherein the step of adjusting the correction coefficients comprises the step of if the correction coefficients is outside a quantization range specified by plus or minus 1/2 of a quantization step size used during the coding process, adjusting the correction coefficient toward the nearest boundary of the quantization range.

12. The method as set forth in claim 1, wherein the step of calculating the correction terms comprises the performing the following equation:

$$A[i][j] = \sum_{k=0}^{k_x-1} \sum_{l=0}^{l_x-1} a_{kl} \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right]$$

where M,N represent the dimensions of the block size,
 $0 \leq i < N, 0 \leq j < M, 0 < k_x \leq N, 0 < l_x \leq M$, and $k_x * l_x$ is the number of correction coefficients used in the correction term.

13. The method as set forth in claim 1, wherein the step of combining further comprises the step of for each pixel in the block, computing, $p'[i][j] = p[i][j] + A[i][j]$ where $p[] []$ is the original blocking image and $p'[] []$ is the produced image exhibiting reduced blocking effects and $A[] []$ are correction terms.

14. A circuit for minimizing blocking effects in a blocking image caused by prior performance of coding processes including transform and quantization processes, said blocking image described by blocking image data, said circuit comprising:

a first low pass filter coupled to received the blocking image data and output filtered image data, said first low pass filter filtering the blocking image data to remove high frequency components above a first identified threshold ;

a blocking error detection subcircuit coupled to receive the filtered image data and the blocking image data and generate blocking error data;

a correction coefficient subcircuit coupled to receive the blocking error data and generating correction coefficients in accordance with the least mean square error of the blocking error data;

correction term subcircuit coupled to receive the correction coefficients and calculating correction terms;

combination subcircuit coupled to receive the blocking image data and the correction terms, said combination subcircuit combining the correction terms and the blocking image data to generate combined image data that exhibits reduced blocking effects.

15. The circuit as set forth in claim 14, further comprising a second low pass filter coupled to receive the combined image data, said second low pass filter filtering out artifacts generated by the combination subcircuit.

16. The circuit as set forth in claim 15, wherein a cutoff frequency of the second low pass filter is higher than a cutoff frequency of the first low pass filter.

17. The circuit as set forth in claim 14, wherein a cutoff frequency of the first low pass filter is set to a frequency such that most of the blocking effects are removed, at the cost of sharpness of the image.

18. The circuit as set forth in claim 15, wherein a cutoff frequency of the second low pass filter is set to a frequency such that only very high frequency components are removed, while sharpness of the image is preserved as much as possible.

19. The circuit as set forth in claim 13, wherein the blocking error detection subcircuit generates blocking error data by subtracting the filtered image data from of the blocking image data.

20. The circuit as set forth in claim 14, wherein the correction coefficient subcircuit determines the correction coefficients a_{kl} according to following equations by applying least mean square errors to the blocking errors of all the pixels in the block:

$$a_{kl} = \frac{-1}{MN} \left\{ \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} E[i][j] \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right] \right\}, \text{ if } k=0, l=0$$

$$= \frac{-2}{MN} \left\{ \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} E[i][j] \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right] \right\}, \text{ if } k=0, l \neq 0 \text{ or } k \neq 0, l=0$$

$$= \frac{-4}{MN} \left\{ \sum_{i=0}^{N-1} \sum_{j=0}^{M-1} E[i][j] \cdot \cos \left[\frac{(2i+1)k\pi}{2N} \right] \cdot \cos \left[\frac{(2j+1)l\pi}{2M} \right] \right\}, \text{ if } k \neq 0, l \neq 0$$

where $E[i][j]$ is the blocking error, $0 \leq k < kx, 0 \leq l < lx, kx \cdot lx$ is the number of coefficients used in the correction term, M and N represent a first and second dimension of the block size..

21. The circuit as set forth in claim 14, wherein the correction coefficient subcircuit determines the correction coefficients a_{kl} by applying LMSE to the blocking errors of only the pixels along the block boundaries as defined by the following:

MSE=

$$\frac{1}{2(M+N)} \left\{ \begin{aligned} & \sum_{i=0}^{N-1} (E[i][0] + A[i][0])^2 \\ & + \sum_{i=0}^{N-1} (E[i][M-1] + A[i][M-1])^2 \\ & + \sum_{j=0}^{M-1} (E[0][j] + A[0][j])^2 \\ & + \sum_{j=0}^{M-1} (E[N-1][j] + A[N-1][j])^2 \end{aligned} \right\}$$

where $E[] []$ represents the blocking error, $A[] []$ represents the correction turn and M, N are the dimensions of the block size.

22. The circuit as set forth in claim 21, wherein, $kx=2$ and $lx=2$ as the correct coefficients comprise a_{00} and a_{01} , a_{10} , a_{11} as defined by the following equations:

$$a_{00} = \frac{-1}{2(M+N)} \left\{ \sum_{i=0}^{N-1} (E[i][0] + E[i][M-1]) + \sum_{j=0}^{M-1} (E[0][j] - E[N-1][j]) \right\}$$

$$a_{10} = \frac{-1}{N + 2M \cos^2 \frac{\pi}{2N}} \left\{ \sum_{i=0}^{N-1} \cos \left[\frac{(2j+1)\pi}{2N} \right] (E[i][0] + E[i][M-1]) + \cos \frac{\pi}{2N} \sum_{j=0}^{M-1} (E[0][j] - E[N-1][j]) \right\}$$

$$a_{01} = \frac{-1}{M + 2N \cos^2 \frac{\pi}{2M}} \left\{ \sum_{j=0}^{M-1} \cos \left[\frac{(2j+1)\pi}{2M} \right] (E[0][j] + E[N-1][j]) + \cos \frac{\pi}{2M} \sum_{i=0}^{N-1} (E[i][0] - E[i][M-1]) \right\}$$

$$a_{11} = \frac{-1}{N \cos^2 \frac{\pi}{2M} + M \cos^2 \frac{\pi}{2N}} \left\{ \cos \frac{\pi}{2M} \sum_{j=0}^{N-1} \cos \left[\frac{(2i+1)\pi}{2N} \right] (E[i][0] - E[i][M-1]) + \cos \frac{\pi}{2N} \sum_{j=0}^{M-1} \cos \left[\frac{(2j+1)\pi}{2M} \right] (E[0][j] - E[N-1][j]) \right\}$$

where $E[] []$ represents the blocking error and M, N are the dimensions of the block size.

23. The circuit as set forth in claim 14, wherein correction term subcircuit calculates the correction terms using the following equation:

$$A[i][j] = \sum_{k=0}^{k_x-1} \sum_{l=0}^{l_x-1} a_{kl} \cdot \cos\left[\frac{(2i+1)k\pi}{2N}\right] \cdot \cos\left[\frac{(2j+1)l\pi}{2M}\right]$$

where M,N represent the dimensions of the block size,

$0 \leq i < N, 0 \leq j < M, 0 < k_x \leq N, 0 < l_x \leq M$, and $k_x * l_x$ is the number of correction coefficients used in the correction term.

24. The circuit as set forth in claim 14 further comprising a Q-level adjustment subcircuit to adjust the correction coefficients to be within the quantization range defined by the quantization step size utilized in the coding process;

wherein the correction term subcircuit receives adjusted correction coefficients to calculate correction terms.

25. The circuit as set forth in claim 14 wherein the Q-level adjustment subcircuit adjusts the correction coefficients towards the nearest boundary of a quantization range specified as plus or minus one half the quantization step size if the correction coefficients are outside the quantization range.

26. The circuit as set forth in claims 14, wherein the combination subcircuit combines the correction terms and blocking image data according to the following equation:

$p'[i][j] = p[i][j] + A[i][j]$, where $p[] []$ is the original blocking image and $p'[] []$ is the produced image exhibiting reduced blocking effects and $A[] []$ are the correction terms.

FIG. 1

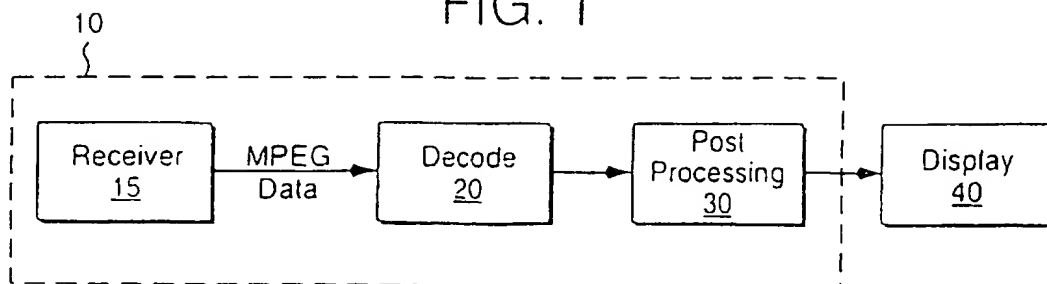


FIG. 2

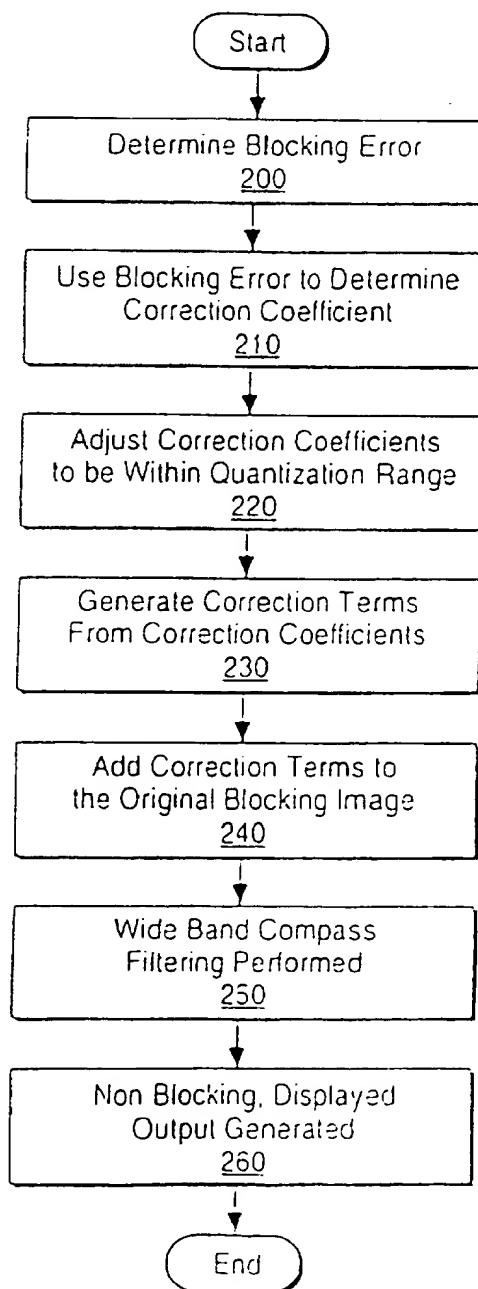


FIG. 3

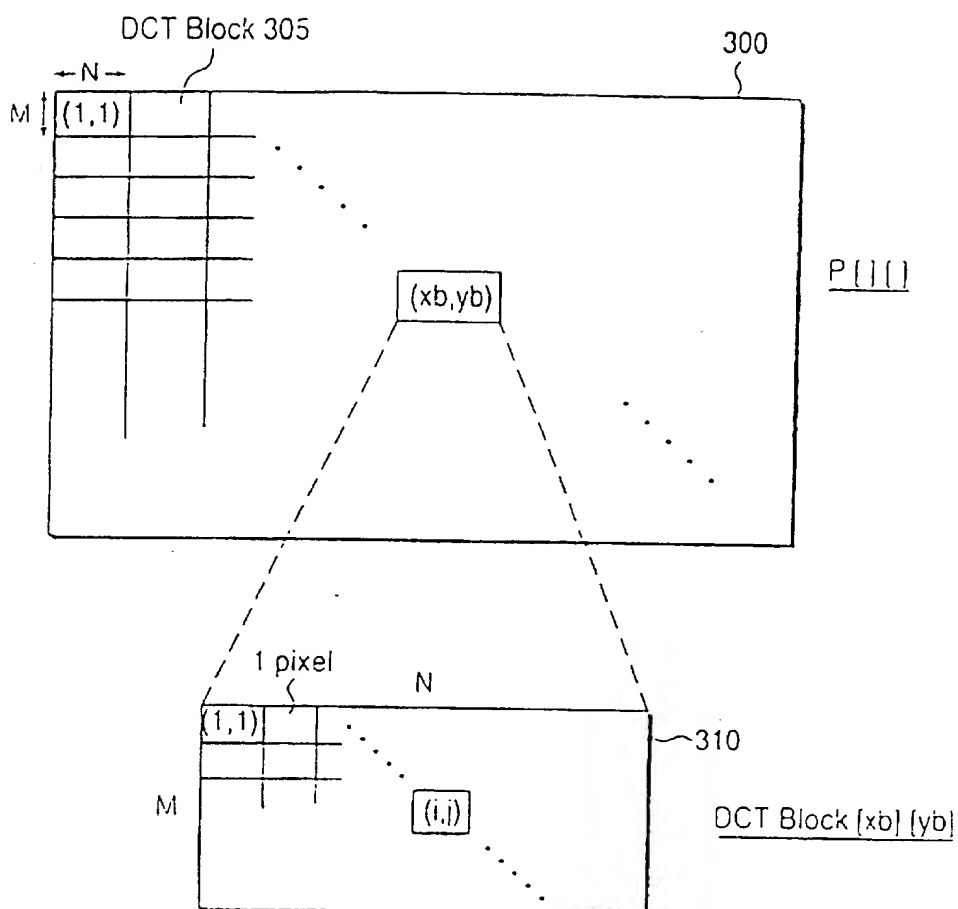
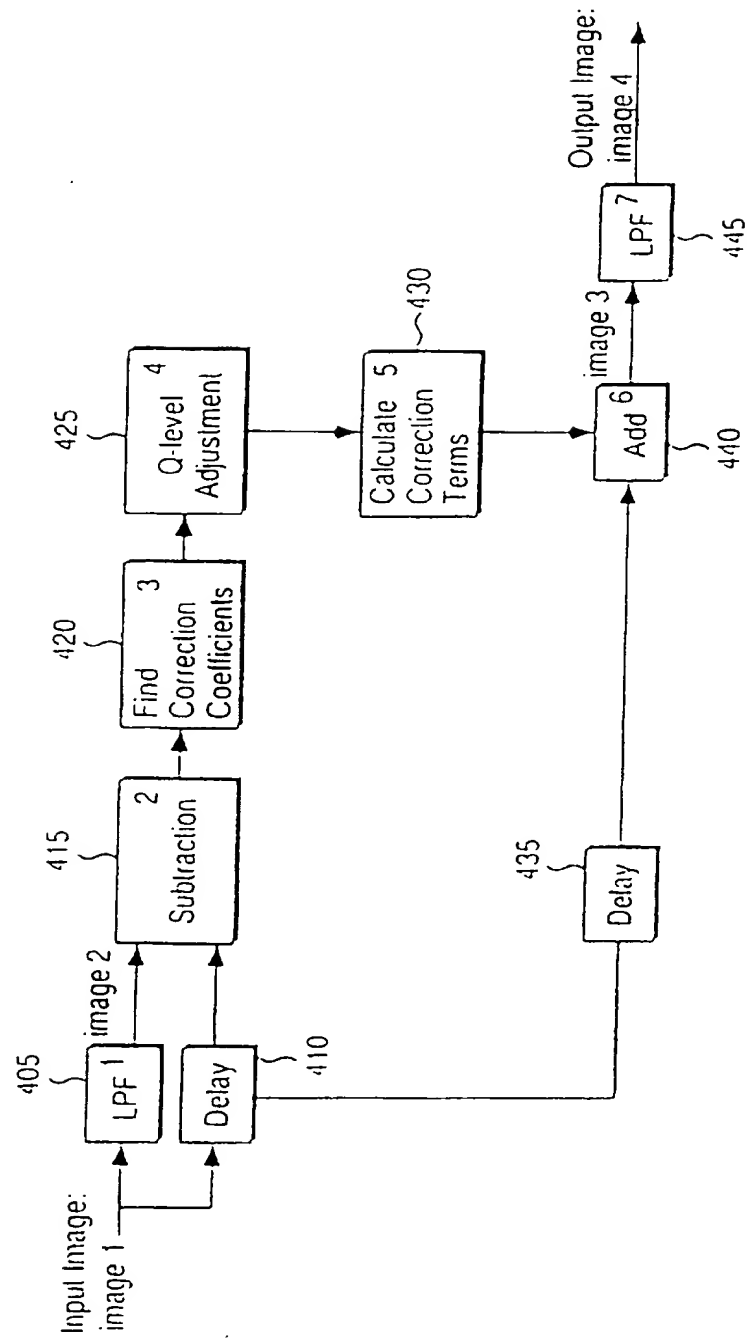


FIG. 4



INTERNATIONAL SEARCH REPORT

International Application No.

/US 97/03292

A. CLASSIFICATION OF SUBJECT MATTER

IPC 6 H04N7/30

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 6 H04N

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	EP 0 680 219 A (DAEWOO ELECTRONICS CO., LTD.) 2 November 1995 see column 4, line 54 - column 7, line 38; figure 2	1-26
A	DE 41 28 977 A (PHILIPS GMBH) 4 March 1993 see page 2, line 49 - page 5, line 33; figure 1	1-26
A	PATENT ABSTRACTS OF JAPAN vol. 95, no. 6, 31 July 1995 & JP 07 087500 A (NTT CORP.), 31 March 1995, see abstract	1-6, 14-19

☒ Further documents are listed in the continuation of box C.

☒ Patent family members are listed in annex.

* Special categories of cited documents:

- *A* document defining the general state of the art which is not considered to be of particular relevance
- *E* earlier document but published on or after the international filing date
- *L* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- *O* document referring to an oral disclosure, use, exhibition or other means
- *P* document published prior to the international filing date but later than the priority date claimed

T later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

X document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

Y document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

& document member of the same patent family

Date of the actual completion of the international search

12 June 1997

Date of mailing of the international search report

30.06.97

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2
NL - 2280 HV Rijswijk
Tel. (+ 31-70) 340-2040, Tx. 31 651 epo nl,
Fax: (+ 31-70) 340-3016

Authorized officer

Foglia, P

INTERNATIONAL SEARCH REPORT

International Application No
PCT/US 97/03292

C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	1993 PICTURE CODING SYMPOSIUM, 17 March 1993, LAUSANNE, CH, pages 13.20.a-13.20.b, XP000346472 OKADA ET AL.: "An Adaptive Image Quality Improvement Method for DCT Coding Schemes" see paragraph 2.4; figure 1 ---	1-6, 14-19
A	EP 0 571 171 A (SAMSUNG ELECTRONICS CO., LTD.) 24 November 1993 see page 4, line 46 - page 7, line 19; figures 3A-6 ---	1-6, 14-19
A	EP 0 590 922 A (XEROX CORP.) 6 April 1994 see page 8, column 11, line 29 - column 12, line 38; claims; figures 8,9 -----	1-6, 14-19

INTERNATIONAL SEARCH REPORT

Information on patent family members

International Application No.

PCT/US 97/03292

Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 680219 A	02-11-95	JP 7303254 A	14-11-95
DE 4128977 A	04-03-93	NONE	
EP 571171 A	24-11-93	CN 1081807 A	09-02-94
		JP 7022962 A	24-01-95
		US 5384849 A	24-01-95
EP 590922 A	06-04-94	US 5379122 A	03-01-95
		JP 6139351 A	20-05-94